JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
KUKATPALLY - HYDERABAD – 500 085
E X A M I N A T I O N   B R A N C H
II YEAR B.TECH - I SEMESTER– R13- II MID TERM EXAMINATIONS, OCTOBER-2015
T I M E   T A B L E

TIME→ FN: 10.00 AM TO 11.30 AM

BRANCH	DATE & DAY					
	26-10-15 FN **MONDAY**	27-10-15 FN **TUESDAY**	28-10-15 FN **WEDENSDAY**	29-10-15 FN **THURSDAY**	30-10-15 FN **FRIDAY**	31-10-15 FN **SATURDAY**
ELECTRICAL AND ELECTRONICS ENGINEERING (02-E E E)	FLUID MECHANICS AND HYDRAULIC MACHINERY	ELECTRONIC DEVICES & CIRCUITS	ELECTRICAL CIRCUITS	ELECTRO MAGNETIC FIELDS	ELECTRICAL MACHINES – I	MATHEMATICS – III
MECHANICAL ENGINEERING (03-M E)	PROBABILITY & STATISTICS	ELECTRICAL AND ELECTRONICS ENGINEERING	MECHANICS OF SOLIDS	ENVIRONMENTAL STUDIES	THERMODYNAMICS	MATALLURGY AND MATERIALS SCIENCE
ELECTRONICS AND COMMUNICATIONS ENGINEERING (04-E C E)	SIGNALS & SYSTEMS	ELECTRONIC DEVICES & CIRCUITS	ELECTRICAL CIRCUITS	SWITCHING THEORY AND LOGIC DESIGN	PROBABILITY THEORY & STOCHASTIC PROCESSES	MATHEMATICS – III
COMPUTER SCIENCE AND ENGINEERING (05-C S E)	PROBABILITY & STATISTICS	ELECTRONIC DEVICES & CIRCUITS	MATHEMATICAL FOUNDATIONS OF COMPUTER SCIENCE	DIGITAL LOGIC DESIGN	DATA STRUCTURES	BASIC ELECTRICAL ENGINEERING
INFORMATION TECHNOLOGY (12- I T)	PROBABILITY & STATISTICS	ELECTRONIC DEVICES & CIRCUITS	MATHEMATICAL FOUNDATIONS OF COMPUTER SCIENCE	DIGITAL LOGIC DESIGN AND COMPUTER ORGANIZATION	DATA STRUCTURES	BASIC ELECTRICAL ENGINEERING

Note:

(i) ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE CONTROLLER OF EXAMINATIONS IMMEDIATELY.

(ii) EVEN IF GOVERNMENT DECLARES HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATIONS SHALL BE CONDUCTED AS USUAL

DATE: 09-10-2015

CONTROLLER OF EXAMINATION

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
KUKATPALLY - HYDERABAD – 500 085
E X A M I N A T I O N   B R A N C H
III YEAR B.TECH - I SEMESTER– R13- II MID TERM EXAMINATIONS, OCTOBER-2015
T I M E   T A B L E

TIME→ AN: 02.00 PM TO 03.30 PM

BRANCH	DATE & DAY					
	26-10-15 AN MONDAY	27-10-15 AN TUESDAY	28-10-15 AN WEDENSDAY	29-10-15 AN THURSDAY	30-10-15 AN FRIDAY	31-10-15 AN SATURDAY
ELECTRICAL AND ELECTRONICS ENGINEERING (02-E E E)	CONTROL SYSTEMS	ELECTRICAL MACHINES-III	IC APPLICATIONS	MANAGEMENT SCIENCE	POWER SYSTEMS-II	POWER ELECTRONICS
MECHANICAL ENGINEERING (03-M E)	DESIGN OF MACHINE MEMBERS–I	DYNAMICS OF MACHINERY	MACHINE TOOLS	ENGINEERING METROLOGY	MANAGERIAL ECONOMICS AND FINANCIAL ANALYSIS	THERMAL ENGINEERING-II
ELECTRONICS AND COMMUNICATIONS ENGINEERING (04-E C E)	COMPUTER ORGANIZATION & OPERATING SYSTEMS	ANTENNAS AND WAVE PROPAGATION	ANALOG COMMUNICATIONS	CONTROL SYSTEMS ENGINEERING	ELECTRONIC MEASUREMENTS AND INSTRUMENTATION	LINEAR AND DIGITAL IC APPLICATIONS
COMPUTER SCIENCE AND ENGINEERING (05-C S E)	COMPILER DESIGN	COMPUTER NETWORKS	SOFTWARE ENGINEERING	OPERATING SYSTEM	PRINCIPLES OF PROGRAMMING LANGUAGES	INTELLECTUAL PROPERTY RIGHTS
INFORMATION TECHNOLOGY (12- I T)	AUTOMATA AND COMPILER	COMPUTER NETWORKS	SOFTWARE ENGINEERING	OPERATING SYSTEM	MANAGERIAL ECONOMICS AND FINANCIAL ANALYSIS	LINUX PROGRAMMING

Note:

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(ii) EVEN IF GOVERNMENT DECLARES HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATIONS SHALL BE CONDUCTED AS USUAL

DATE: 09-10-2015

CONTROLLER OF EXAMINATION

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
KUKATPALLY - HYDERABAD – 500 085
E X A M I N A T I O N   B R A N C H
IV YEAR B.TECH - I SEMESTER– R09- II MID TERM EXAMINATIONS, OCTOBER-2015
T I M E   T A B L E

TIME➔ FN: 10.00 AM TO 11.30 AM

BRANCH	DATE & DAY						
	26-10-15 FN **MONDAY**	27-10-15 FN **TUESDAY**	28-10-15 FN **WEDENSDAY**	29-10-15 FN **THURSDAY**	30-10-15 FN **FRIDAY**	31-10-15 FN **SATURDAY**	02-11-15 FN **MONDAY**
ELECTRICAL AND ELECTRONICS ENGINEERING (02-E E E)	SWITCHGEAR AND PROTECTION	UTILIZATION OF ELECTRICAL ENERGY	INSTRUMENTATION	POWER SYSTEM OPERATION AND CONTROL	HIGH VOLTAGE ENGINEERING	ELECTRICAL DISTRIBUTION SYSTEMS	--
MECHANICAL ENGINEERING (03-M E)	OPERATION RESEARCH	POWER PLANT ENGINEERING	CAD/CAM	INSTRUMENTATION AND CONTROL SYSTEMS	ROBOTICS	DESIGN FOR MANUFACTURING	--
ELECTRONICS AND COMMUNICATIONS ENGINEERING (04-E C E)	MANAGEMENT SCIENCE	MICROWAVE ENGINEERING	EMBEDDED SYSTEMS	--	COMPUTER NETWORKS	DIGITAL IMAGE PROCESSING	VLSI DESIGN
COMPUTER SCIENCE AND ENGINEERING (05-C S E)	--	INFORMATION RETRIEVAL SYSTEMS	COMPUTER GRAPHICS	MOBILE COMPUTING	DATA WAREHOUSING AND DATA MINING	SOFTWARE TESTING METHODOLOGIES	LINUX PROGRAMMING
INFORMATION TECHNOLOGY (12- I T)	IMAGE PROCESSING AND PATTERN RECOGNITION	INFORMATION RETRIEVAL SYSTEMS	OBJECT ORIENTED ANALYSIS AND DESIGN	MOBILE APPLICATION DEVELOPMENT	--	SOFTWARE TESTING METHODOLOGIES	VLSI DESIGN

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(ii) EVEN IF GOVERNMENT DECLARES HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATIONS SHALL BE CONDUCTED AS USUAL

(iii) READMITTED   STUDENTS HAVE TO APPEAR FOR THE SUBSTITUTE SUBJECT(S)   [WHICH IS/ARE   NOT SHOWN IN THE TIME-TABLE] IN PLACE OF   THE SUBJECT(S) ALREADY PASSED.

DATE: 09-10-2015

CONTROLLER OF EXAMINATION