

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

KUKATPALLY - HYDERABAD – 500 085

EXAMINATION BRANCH

IV YEAR B.TECH - I SEMESTER– R13- II MID TERM EXAMINATIONS, NOVEMBER, 2016

TIME TABLE

TIME FN: 10.00 AM TO 11.30 AM & AN: 02:00 PM TO 03:30 PM

BRANCH	05-11-2016 (FN) SATURDAY	07-11-2016 FN MONDAY	07-11-2016 AN MONDAY	08-11-16 FN TUESDAY	09-11-2016 FN WEDNESDAY	10-11-2016 FN THURSAY
ELECTRICAL AND ELECTRONICS ENGINEERING (02-E E E)	Switch Gear and Protection	Utilization of Electrical Energy	Digital Signal Processing	Power System Operation and Control	High Voltage Engineering	Electrical Distribution Systems
MECHANICAL ENGINEERING (03-M E)	Operations Research	Power Plant Engineering	CAD/CAM	Instrumentation and Control Systems	Industrial Management	Unconventional Machining Processes
ELECTRONICS AND COMMUNICATIONS ENGINEERING (04-E C E)	Management Science	Computer Networks	Microwave Engineering	Cellular and Mobile Communications	Digital Image Processing	Embedded Systems Design
COMPUTER SCIENCE AND ENGINEERING (05-C S E)	Cloud Computing	Design Patterns	Data Warehousing and Data Mining	Linux Programming	Software Project Management	Information Retrieval Systems
INFORMATION TECHNOLOGY (12- I T)	Information Security	Design Patterns	Mobile Application Development	Information Retrieval Systems	Image Processing and Pattern Recognition	Human Computer Interaction

Note:

- (i) ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE CONTROLLER OF EXAMINATIONS IMMEDIATELY.
- (ii) EVEN IF GOVERNMENT DECLARES HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATIONS SHALL BE CONDUCTED AS USUAL
- (iii) READMITTED STUDENTS HAVE TO APPEAR FOR THE SUBSTITUTE SUBJECT(S) [WHICH IS/ARE NOT SHOWN IN THE TIME-TABLE] IN PLACE OF THE SUBJECT(S) ALREADY PASSED. FOR DETAILS OF SUBSTITUTE SUBJECTS REFER THE COMMUNICATIONS RECEIVED FROM THE DIRECTOR OF ACADEMIC & PLANNING.

DATE: 13-10-2016

Sd/-
CONTROLLER OF EXAMINATIONS

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD**3-1 (R13) II MID****KUKATPALLY - HYDERABAD – 500 085****EXAMINATION BRANCH****III YEAR B.TECH - I SEMESTER– R13- II MID TERM EXAMINATIONS, NOVEMBER-2016****TIME TABLE****TIME → FN: 10.00 AM TO 11.30 AM****AN: 02.00 PM TO 03.30 PM**

BRANCH	DATE & DAY					
	05-11-2016 AN SATURDAY	07-11-2016 FN MONDAY	07-11-2016 AN MONDAY	08-11-2016 AN TUESDAY	09-11-2016 AN WEDNESDAY	10-11-2016 AN THURSDAY
ELECTRICAL AND ELECTRONICS ENGINEERING (02-E E E)	CONTROL SYSTEMS	ELECTRICAL MACHINES-III	IC APPLICATIONS	MANAGEMENT SCIENCE	POWER SYSTEMS-II	POWER ELECTRONICS
MECHANICAL ENGINEERING (03-M E)	DESIGN OF MACHINE MEMBERS-I	DYNAMICS OF MACHINERY	MACHINE TOOLS	ENGINEERING METROLOGY	MANAGERIAL ECONOMICS AND FINANCIAL ANALYSIS	THERMAL ENGINEERING-II
ELECTRONICS AND COMMUNICATIONS ENGINEERING (04-E C E)	COMPUTER ORGANIZATION AND OPERATING SYSTEMS	ANTENNAS AND WAVE PROPAGATION	ANALOG COMMUNICATIONS	CONTROL SYSTEMS ENGINEERING	ELECTRONIC MEASUREMENTS AND INSTRUMENTATION	LINEAR AND DIGITAL IC APPLICATIONS
COMPUTER SCIENCE AND ENGINEERING (05-C S E)	COMPILER DESIGN	COMPUTER NETWORKS	SOFTWARE ENGINEERING	OPERATING SYSTEMS	PRINCIPLES OF PROGRAMMING LANGUAGES	INTELLECTUAL PROPERTY RIGHTS
INFORMATION TECHNOLOGY (12- I T)	AUTOMATA AND COMPILER DESIGN	COMPUTER NETWORKS	SOFTWARE ENGINEERING	OPERATING SYSTEMS	MANAGERIAL ECONOMICS AND FINANCIAL ANALYSIS	LINUX PROGRAMMING

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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD**KUKATPALLY - HYDERABAD – 500 085****EXAMINATION BRANCH****II YEAR B.TECH - I SEMESTER – R15- II MID TERM EXAMINATIONS, NOVEMBER- 2016****TIME TABLE**TIME → **FN**: 10.00 AM TO 11.30 AM & **AN**: 02:00 PM TO 03:30 PM

BRANCH	DATE, TIME & DAY					
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ELECTRICAL AND ELECTRONICS ENGINEERING (02-E E E)	FLUID MECHANICS AND HYDRAULIC MACHINERY	ELECTRONIC DEVICES & CIRCUITS	ELECTRICAL CIRCUITS	ELECTRO MAGNETIC FIELDS	ELECTRICAL MACHINES - I	MATHEMATICS – III
MECHANICAL ENGINEERING (03-M E)	PROBABILITY & STATISTICS	ELECTRICAL AND ELECTRONICS ENGINEERING	MECHANICS OF SOLIDS	ENVIRONMENTAL STUDIES	THERMO - DYNAMICS	MATALLURGY AND MATERIALS SCIENCE
ELECTRONICS AND COMMUNICATIONS ENGINEERING (04-E C E)	SIGNALS & SYSTEMS	ELECTRONIC DEVICES & CIRCUITS	ELECTRICAL CIRCUITS	SWITCHING THEORY AND LOGIC DESIGN	PROBABILITY THEORY & STOCHASTIC PROCESSES	MATHEMATICS – III
COMPUTER SCIENCE AND ENGINEERING (05-C S E)	PROBABILITY & STATISTICS	ELECTRONIC DEVICES & CIRCUITS	MATHEMATICAL FOUNDATIONS OF COMPUTER SCIENCE	DIGITAL LOGIC DESIGN	DATA STRUCTURES	BASIC ELECTRICAL ENGINEERING
INFORMATION TECHNOLOGY (12- I T)	PROBABILITY & STATISTICS	ELECTRONIC DEVICES & CIRCUITS	MATHEMATICAL FOUNDATIONS OF COMPUTER SCIENCE	DIGITAL LOGIC DESIGN AND COMPUTER ORGANIZATION	DATA STRUCTURES	BASIC ELECTRICAL ENGINEERING

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