

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

KUKATPALLY-HYDERABAD-500085

EXAMINATION BRANCH

M.TECH .I YEAR -I SEMESTER -R19 REGULATION- REGULAR/SUPPLEMENTARY EXAMINATIONS JULY/AUGUST-2021

T I M E T A B L E

TIME: 09:45 AM TO 12:45PM

Course	24-07-2021 SATURDAY	27-07-2021 TUESDAY	29-07-2021 THURSDAY	31-07-2021 SATURDAY	16-08-2021 MONDAY	18-08-2021 WEDNESDAY	23-08-2021 MONDAY
ELECTRICAL POWER SYSTEMS -07	Power System Analysis	Economic Operation of Power Systems	Modern Control Theory	***	***	Research Methodology and IPR	Electrical Power Distribution System
ENGINEERING DESIGN. - 95	Advanced Mechanics of Solids	Advanced Mechanics of Machinery	***	Advanced Machine Design	***	Research Methodology and IPR	Design for Manufacturing & Assembly
DIGITAL SYSTEMS AND COMPUTER ELECTRONICS - 06	***	Digital Systems Design with PLDs	Advanced Data Communications	VLSI Technology and Design	Embedded System Design	Research Methodology and IPR	***
COMPUTER SCIENCE AND ENGINEERING - 58	Mathematical Foundations of Computer Science	Advanced Data Structures	***	Cloud Computing	***	Research Methodology and IPR	Information Security
INFORMATION TECHNOLOGY - 40	Mathematical Foundations of Computer Science	Advanced Data Structures	Python Programming	***	Software Project Management	Research Methodology and IPR	***

DATE: 12/07/2021

Sd/-
CONTROLLER OF EXAMINATION

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EXAMINATION BRANCH

M.TECH. I YEAR -II SEMESTER -R19 REGULATION SUPPLEMENTARY EXAMINATIONS JULY/AUGUST-2021

T I M E T A B L E

TIME : 09:45 AM TO 12:45 PM

COURSE	26-07-2021 MONDAY	28-07-2021 WEDNESDAY	30-07-2021 FRIDAY	07-08-2021 SATURDAY	17-08-2021 TUESDAY	21-08-2021 SATURDAY	24-08-2021 TUESDAY
ELECTRICAL POWER SYSTEMS – 07	Digital Protection of Power System			Swarm Intelligence Techniques in Power Systems	Power System Dynamics	Power Quality	
ENGINEERING DESIGN -95		Advanced Finite Element and Boundary Element Methods	Industrial Robotics		Computer Aided Geometric Modeling	Design and Analysis of Experiments	
DIGITAL SYSTEM &COMPUTER ELECTRONICS -06	Advanced Computer Architecture	Design of Fault Tolerant Systems		Low Power VLSI			Embedded Real Time Operating Systems
COMPUTER SCIENCE ENGINEERING - 58	Advanced Computer Architecture		Internet of Things	Advanced Algorithms		Advanced Computer Networks	
INFORMATION TECHNOLOGY-40	Machine Learning	Web Technologies	Information Security			Big Data Analytics	

DATE: 12/07/2021

Sd/-
CONTROLLER OF EXAMINATIONS

**NOTE: (i) ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE CONTROLLER OF EXAMINATIONS IMMEDIATELY
(ii) EVEN IF GOVERNMENT DECLARES HOLIDAY ON ANY OF THE ABOVE DATES.THE EXAMINATIONS SHALL BE CONDUCTED AS US**