JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

KUKATPALLY - HYDERABAD - 500085 EXAMINATION BRANCH

<u>II YEAR B.TECH –II SEMESTER – R18 REGULATION - I MID TERM EXAMINATIONS MAY/JUNE-2021</u>

TIMETABLE

TIME→ FN: 9.30 AM TO 10.50 AM AN: 1.30 PM TO 2.50 PM

BRANCH	DATE, SESSION AND DAY							
	31-05-2021 FN MONDAY	31-05-2021 AN MONDAY	01-06-2021 FN TUESDAY	01-06-2021 AN TUESDAY	02-06-2021 FN WEDNESDAY			
ELECTRICAL AND ELECTRONICS ENGINEERING (02- EEE)	Laplace Transforms, Numerical Methods & Complex variables	Electrical Machines – II	Control Systems	Power System - I	Digital Electronics			
MECHANICAL ENGINEERING (03- ME)	Basic Electrical and Electronics Engineering	Kinematics of Machinery	Thermal Engineering - I	Fluid Mechanics and Hydraulic Machines	Instrumentation and Control Systems			
ELECTRONICS & COMMUNICATIONS ENGINEERING (04- ECE)	Laplace Transforms, Numerical Methods & Complex Variables	Electromagnetic Fields and Waves	Analog and Digital Communications	Linear IC Applications	Electronic Circuit Analysis			
COMPUTER SCIENCE & ENGINEERING (05- CSE)	Discrete Mathematics	Business Economics & Financial Analysis	Operating Systems	Database Management Systems	Java Programming			
INFORMATION TECHNOLOGY (12-IT)	Discrete Mathematics	Business Economics & Financial Analysis	Operating Systems	Database Management Systems	Java Programming			

DATE: 27-05-2021 Sd/CONTROLLER OF EXAMINATIONS

NOTE:

- 1) ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE CONTROLLER OF EXAMINATIONS IMMEDIATELY.
- II) EVEN IF GOVERNMSNTT DECLARES HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATIONS SHALL BE CONDUCTED AS USUAL.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

KUKATPALLY - HYDERABAD - 5000 85 EXAMINATION BRANCH

HI YEAR B.TECH - II SEMESTER- R18 REGULATION I - MID TERM EXAMINATIONS MAY/JUNE-2021 TIMETABLE

TIME→ FN: 11.30 AM TO 12.50 PM AN: 3.30 PM TO 4.50 PM

BRANCH	31-05-2021 FN MONDAY	31-05-2021 AN MONDAY	01-06-2021 FN TUESDAY	01-06-2021 AN TUESDAY	02-06-2021 FN WEDNESDAY	02-06-2021 AN WEDNESDAY
ELECTRICAL AND ELECTRONICS ENGINEERING (02- EEE)	Signals and Systems	Microprocessors & Microcontrollers	Power System Protection	Power Semiconductor Drives	Power System Operation and Control	Cyber Law & Ethics
MECHANICAL ENGINEERING (03- ME)	Design of Machine Members-II	CAD & CAM	Heat Transfer	Unconventional Machining Processes	Finite Element Methods	Renewable Energy Sources
ELECTRONICS& COMMUNICATIONS ENGINEERING (04- ECE)	Antennas and Propagation	Digital Signal Processing	_	Embedded System Design	VLSI Design	Cyber Law & Ethics
COMPUTER SCIENCE & ENGINEERING (05- CSE)	Machine Learning	Compiler Design	Design and Analysis of Algorithms	Software Testing Methodologies	_	Fundamentals of Internet of Things
INFORMATION TECHNOLOGY (12-IT)	Introduction to Embedded Systems	Principles of Compiler Construction	Algorithm Design and Analysis	Software Testing Methodologies	Internet of Things	Quantitative Analysis for Business Decisions

Date: 27-05-2021

Sd/CONTROLLER OF EXAMINATIONS

Note:

- I. ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE CONTROLLER OF EXAMINATIONS IMMEDIATELY.
- II. EVEN IF GOVERNMENT DECLARES HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATIONS SHALL BE CONDUCTED AS USUAL
- III. READMITTED STUDENTS HAVE TO APPEAR FOR THE SUBSTITUTE SUBJECT(S) [WHICH IS/ARE NOT SHOWN IN THE TIME-TABLE] IN PLACE OF THE SUBJECT(S) ALREADY PASSED. FOR DETAILS OF SUBSTITUTE SUBJECTS REFER THE COMMUNICATIONS RECEIVED FROM THE DIRECTOR OF ACADEMIC & PLANNING.