



JNTUH COLLEGE OF ENGINEERING JAGTIAL

Nachupally (Kondagattu), Jagtial Dist. 505501, Telangana State.

Department of Electronics & Communication Engineering

Academic Year : 2018-19

Workshop / Seminar / Training Programme / FDPs Organised by Faculty				
S.No.	Name of the Faculty	Name of the Workshop/Seminar/ Training Programme	Dates	Place
1	Course Coordinator: Dr. Dhiraj Sunehra Co-Coordinator: Smt. D. Naga Sudha	A Three Workshop on Cadence UG Bundle VLSI Tools in Association with M/s. Entuple Technologies, Bangalore.	29-31 Aug, 2018	JNTUHCEJ
2	ISF Coordinator: Dr. Dhiraj Sunehra	“Traditio 2K19”-Technical Presentation Competition organized under IETE Students Forum(ISF)	8 th March 2019	JNTUHCEJ
3	Convener: Dr. Dhiraj Sunehra Co-Convener: Sri. S. Praveen Kumar	A National Level Technical Symposium (EMBLAZON-19)	April 2-3, 2019	JNTUHCEJ
4	Course Coordinator: Dr. Dhiraj Sunehra	A Two Day National Workshop on Electronic Circuit Simulation Using NI-Multisim Software	April 2-3, 2019	JNTUHCEJ
5	Course Coordinator: Dr. Dhiraj Sunehra	A Two Day Training Programme on Electronic Circuit Simulation Using NI Multisim Software	April 25-26, 2019	JNTUHCEJ

**A Three Workshop on Cadence UG Bundle VLSI Tools in Association with
M/s. Entuple Technologies, Bangalore**





Email Communication

Re: Cadence EDA tool training schedule req.

From: Dhiraj Sunehra (dhirajsunehra@yahoo.co.in)
To: sharath.kanth@entuple.com
Date: Thursday, 23 August, 2018, 3:07 pm IST

Dear Sir,

Thank you for the email. I am confirming the 3 days training on Cadence UG bundle tools (Analog & Digital FE & BE) at our campus from 29-31 August 2018. I request you to kindly provide the day wise schedule of training for the three days.

Thanking you,
Best regards,
Dr. Dhiraj Sunehra
Prof. & Head, ECE
JNTUH CE, Jagtial
Ph. 7893278314

[Sent from Yahoo Mail on Android](#)

On Thu, 23 Aug 2018 at 11:06 am, Sharath Kanth <sharath.kanth@entuple.com> wrote:

Dear Dhiraj,

Greetings from Entuple Technologies....

In reference to our discussion regarding the training on Cadence EDA tools at your campus is scheduled from t 29th to 31st of August 2018. Please confirm the same from your side.

We are presently working on preparing your lab manuals and the soft copy of which will be mailed to you shortly.

M Sharath Kanth | Sr Application Engineer - Cadence

2730, 'Trikanika', 80 Feet Road, HAL 3rd Stage Indiranagar,
Bengaluru, Karnataka 560038| Mobile : +91-7893234547
Web: www.entuple.com



AGENDA

Day 1: Fundamentals of Standard Cell Modules - Design and Simulation of the

Balanced inverter, Layout Design Concepts

Session 1

1. Introduction to VLSI
2. CMOS inverter and its applications
 - Concept of balanced, symmetric and fastest inverter
 - **Hands - on lab: Design and simulation of a CMOS inverter(gpdk180)**
 - DC and Transient Analysis
 - Analysis of the quality metrics of designed inverter

Session 2

1. Custom IC design flow of designed balanced inverter
2. **Hands - on Lab**
 - Layout capture
 - Physical verification (DRC and LVS)
 - Parasitic extraction (parasitic Resistance and Capacitance)
 - Post Layout Simulation with parasitic back annotation
 - GDSII of the layout

Day 2: Digital ASIC Design

Session 1: Front End Design (Design Entry to Synthesis)

1. Introduction to ASIC design flow
2. CADENCE platform for digital design
 - Incisive
 - RTL Compiler
 - Encounter
3. Functional Simulation
 - Using GUI
4. HDL Capturing and Functional Simulation of HDL codes.
 - Basic gates
 - Counter
5. Synthesis



Session 2: BACKEND DESIGN (RTL to GDSII)

1. Physical Design of counter HDL Design

- Floor Planning
- Power Planning
- Placement
- CTS
- Routing
- Timing Analysis

2. Generating Stream data file (GDSII) & post layout files

Day 3: Curriculum based demos

Session 1: Front-end Design / curriculum Part-I

- Understanding Verilog designs
- Writing test cases for the designs
- Simulation of Verilog designs developed
- Synthesizing the design using GPDKs

Session 2: Custom IC Design flow / Curriculum part -II

- Understanding schematics / simulation environments
- Simulation procedures / test cases
- Layouts
- Physical verification.

Traditio 2K19 Brochure

Steering Committee:

Prof. N.V. Ramana
Principal, JNTUHCEJ

Prof. S. Viswanadha Raju
Vice Principal, JNTUHCEJ

Prof. Dhiraj Sunehra
Head, ECE & Faculty
Coordinator, ISF JNTUHCEJ

Organizing Committee:

President:
Phanindra Edukulla

Vice President:
T. Saimanish

General Secretary:
Rohan Majumdar

Treasurer:
K. Pravalika

Joint-Secretary:
B. Naga Poojitha

Members:
T. Harsha Vardhan
Md. Shahebaz
S. Sowmya
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G. Nitish
B. Venkatesh
B. Jhansi
V. Sai Sreshtha
M. Srinidhi
V. Ratan
P. Mani Pranav
P. Sahithi
Farozan

For Further details Please Contact Coordinators:

E. Phanindra
Ph.9505903948
T. Saimanish
Ph.8500836243
K. Pravalika
Ph.939843263
R. Nagendra
Ph.8801774493
B. Naga Poojitha

JNTUH COLLEGE OF ENGINEERING JAGTIAL



DEPARTMENT OF ECE IETE STUDENT FORUM PRESENTS



TRADITIO 2K19

Call for Abstracts for Technical Presentation

IETE Student Forum (ISF) at JNTUH CEJ is formed with twin objectives of counseling the students on new emerging opportunities, and to encourage and motivate students to organize and participate in various Workshops/Projects/Seminars, etc.

As part of ISF activities, it is planned to organize a **Technical Presentation Competition (Traditio 2K19)** for Undergraduate Students of **Electronics and Communication Engineering (I-IV Year B.Tech.)** of JNTUH CEJ on Friday, **March 8th, 2019**.

Traditio 2K19 aims at providing the students an opportunity to showcase their technical and presentation skills.

Submission of Abstracts:

A One-Page neatly typed Abstract on any current and emerging topic in the field of ECE should be submitted on or before **6th March 2019** to the Coordinators.

- Each Team should have two members of the same year.
- Selected Abstracts will be short listed for Oral PPT Presentation, based on the topic and clarity of the abstract.
- Certificates will be awarded for three best presentations, in each year of study.

Registration:

No Registration Fee.

Topics: (not limited to)

- ✓ Electronic Devices & Circuits
- ✓ Digital Electronics & VLSI
- ✓ Advanced Microprocessors, Microcontrollers, Embedded Systems and IoT
- ✓ Analog and Digital Communications
- ✓ Microwave, Satellite and Optical Communications
- ✓ Cellular and Mobile Communications
- ✓ Wireless Sensor Networks, Vehicular Networks and Cognitive Radio
- ✓ Machine Learning, Artificial Intelligence and Deep Learning
- ✓ Image, Video & Speech Processing
- ✓ Multimedia Applications
- ✓ Future Internet Applications & Services : E-Health, Social Networks

Important Deadlines:

- **Submission of Abstract**
March 6th, 2019

Venue and Date
MPMC LAB
Dept. of ECE, JNTUH CEJ
8th MARCH 2019

Traditio 2K19 Participants List



JNTUH College of Engineering Jagtial

IETE STUDENT FORUM

TRADITIO 2K19

(Technical Presentation Competition)

Date: 8th March 2019

Venue: MPMC Lab, Dept. of ECE



B.Tech I Year (ECE)

Time: 02:00PM to 06:00PM

S.NO	PRESENTER'S NAME	ROLL NO.	TOPIC	SIGN
1	T. LAHARI MAHESHWARI	18JJ1A0456 18JJ1A0458	GPS MAPTOR	
2	T.VINEESHA S.SRIJA	18JJ1A0448 18JJ1A0442	ARTIFICIAL INTELLIGENCE	
3	V.SPOORTHY PRIYA K.MADHURI	18JJ1A0457 18JJ1A0425	SMART DUST	
4	CH.KAVYA S.BHAVANI	18JJ1A0411 18JJ1A0445	WEARABLE BIO SENSORS	
5	J.ARAVIND P.PAVAN KUMAR	18JJ1A0453 18JJ1A0437	SIM CARD COMMUNICATION	
6	BALA SWAPNIKA AFIFA	18JJ1A0419 18JJ1A0401	INTERNET OF THINGS	
7	MAHESH MANIKANTA	18JJ1A0409 18JJ1A0402	WIRELESS AND CELLULAR COMMUNICATION	
8	ASRITHA SUSHMA	18JJ1A0411 18JJ1A0447	ARTIFICIAL NEURAL NETWORK	
9	CHANDRIKA PAVAN KALYAN	18JJ1A0424 18JJ1A0423	GI-FI	
10	KAMALJEET SINGH SAI RAM	18JJ1A0443 18JJ1A0450	NANO ELECTRONICS	
11	MOKSHITH GRAHESH	18JJ1A0431 18JJ1A0435	GPS	
12	S.PREMCHAND MD.YASIN	18JJ1A0441 18JJ1A0434	SIXTH SENSE TECHNOLOGY	
13	B.SURESH P.NETHRANAND	18JJ1A0406 18JJ1A0440	ZIGBEE	
14	ANIL SATYADEEP	18JJ1A0414 18JJ1A0403	LASER COMMUNICATION	
15	VANDANA SATWIKA	18JJ1A0427 18JJ1A0428	VISIBLE LIGHT COMMUNICATION	
16	MADHU CHANDU	18JJ1A0431 18JJ1A0436	BIOCHIP TECHNOLOGY	



JNTUH College of Engineering Jagtial



IETE STUDENT FORUM

TRADITIO 2K19

(Technical Presentation Competition)

Date: 8th March 2019

Venue: MPMC Lab, Dept. of ECE

B.Tech II Year (ECE)

Time: 02:00PM to 06:00PM

S.NO	PRESENTER'S NAME	ROLL. NO	TOPIC	SIGN
1.	PORIKA CHANDANA	17JJ1A0433	AUGMENTED REALITY	
2	B.NAGA POOJITHA AND B.AKANKSHA	17JJ1A0429 AND 17JJ1A0404	MACHINE LEARNING	
3	FAROZAN AND THAMKEEN SIDDIKA	17JJ1A0411 AND 17JJ1A0450	CELLONICS TECHNOLOGY	
4	FARZANA AND KHUSHBOO	17JJ1A0411 AND 17JJ1A0420	LI-FI TECHNOLOGY	
5	R.NAGENDRA BABU	17JJ1A0444	MACHINE LEARNING	
6	K. MEGHANA REDDY AND R. SRI VALLIKA REDDY	17JJ1A0423 AND 17JJ1A0442	5G TECHNOLOGY	
7	N. SAI PRANAY AND V. RATHAN	17JJ1A0430 AND 17JJ1A0454	OPTICAL COMPUTERS	
8	P. SAHITHI AND K. SATWIKA	17JJ1A0438 AND 17JJ1A0416	LASER COMMUNICATION	
9	L.GOPI AND K.SANDEEP	17JJ1A0425 AND 17JJ1A0422	CELL PHONE JAMMER	
10	L.POOJA AND AKHILA	18JJ5A0403 AND 18JJ5A0407	OPTICAL FIBRE	
11	IVANJILIN AND AAMINA	17JJ1A0413 AND 17JJ1A0448	3-G HOLOGRAPHIC PROJECT	
12	KEERTHI AND TANMAI	17JJ1A0424 AND 17JJ1A0446	E-BALL	
13	B.KEERTHI P.SAIRANI	18JJ5A0402 18JJ5A0409	VLSI TECHNOLOGY	
14	P.MANI PRANAV V.MAHENDER	17JJ1A0441 17JJ1A0456	ARTIFICIAL INTELLIGENCE	



JNTUH College of Engineering Jagtial



IETE STUDENT FORUM

TRADITIO 2K19

(Technical Presentation Competition)

Date: 8th March 2019

Venue: MPMC Lab, Dept. of ECE

B.Tech III Year (ECE)

Time: 02:00PM to 06:00PM

S.NO	PRESENTER'S NAME	ROLL.NO	TOPIC	SIGN
1	A.SAI SHASHANK P.SRIKANTH	16JJ1A0402 17JJ5A0405	SMART ANTENNAS	
2	S.MANEESHA K.PRAVALIKA	16JJ1A0450 16JJ1A0427	RADIO FREQUENCY IDENTIFICATION TECHNOLOGY	
3.	GARE VARMA KRISHNA	16JJ1A0421	CONCEALED WEAPON DETECTION USING IMAGE PROCESSING	
4	S.JYOTHI M.NAGAMANI	16JJ1A0448 16JJ1A0433	GI-FI TECHNOLOGY	
5	D.PRIYA VARENYA CH.MANISHA	16JJ1A0414 16JJ1A0410	ADAPTIVE MISSILE GUIDANCE USING GPS	
6	B.JHANSI	16JJ1A0408	PERCEPTRON	
7	B.MANISHA	16JJ1A0406	IRIS BIOMETRIC DETECTION SYSTEM	
8	MADIHA ARFEEN N.LAHARI	16JJ1A0431 16JJ1A0439	SPINTRONICS	
9	R.VAMSHIVARDHAN GOUD D.SAI KRISHNA	16JJ1A0445 16JJ1A0415	MACHINE LEARNING	
10	B.PREETHI N.MOUNIKA	16JJ1A0405 16JJ1A0438	GRAPHENE TRANSISTOR	
11	V.SAI SRESHTA M.SRINIDHI	16JJ1A0460 16JJ1A0432	PLASMONICS	
12	G.NITHISH	16JJ1A0420	HOLOGRAPHIC DATA STORAGE	
13	M.PRATHYUSHA	16JJ1A0434	SMART VOTING SYSTEM SUPORT THROUGH FACE DETECTION	
14	NIKHIL	16JJ1A0401	SATELLITE COMMUNICATION	
15	SOWMYA	16JJ1A0426	3-D INTERNET	

Students giving their Presentation in Traditio 2K19





Sample Certificate – Traditio 2K19

CERTIFICATE
OF PARTICIPATION

JNTUH COLLEGE OF ENGINEERING JAGTIAL
Nachupally (Kondagattu), Jagtial Dist.-505 501, Telangana

IETE STUDENTS FORUM

*This is to certify that Mr./Ms. _____
_____ has participated in the
Technical Presentation Competition - TRADITIO 2K19 organised by the
IETE Students Forum (ISF) of JNTUH College of Engineering, Jagtial
held on March 6, 2019.
Title of Presentation: _____*

 **Dr. Dhiraj Sunehra**
Professor & Head, ECE
Faculty Coordinator, ISF

Dr. N.V. Ramana
Professor of EEE &
PRINCIPAL

CERTIFICATE
OF APPRECIATION

JNTUH COLLEGE OF ENGINEERING JAGTIAL
Nachupally (Kondagattu), Jagtial Dist.-505 501, Telangana

IETE STUDENTS FORUM

*This is to certify that Mr./Ms. _____
_____ has won the _____ Prize in the
Technical Presentation Competition - TRADITIO 2K19 organised by the
IETE Students Forum (ISF) of JNTUH College of Engineering, Jagtial
held on March 6, 2019.
Title of Presentation: _____*

 **Dr. Dhiraj Sunehra**
Professor & Head, ECE
Faculty Coordinator, ISF

Dr. N.V. Ramana
Professor of EEE &
PRINCIPAL

Emblazon'19 – A National Level Technical Symposium



Phone: +91-7893278314, 9440268314

Email: emblazon2k19ece@gmail.com

Website: www.jntuhcej.ac.in

Jawaharlal Nehru Technological University Hyderabad College of Engineering Jagtial

Nachupally (Kondagattu), Jagtial Dist - 505 501, Telangana

Prof. N.V. Ramana

Professor of EEE &
PRINCIPAL

No. JNTUHCE/Symp/Emblazon/19

Date: 7th March 2019

Dear Sir/Madam,

Sub: Call for Participation - EMBLAZON'19 - A National Level Technical Symposium

JNTUH College of Engineering Jagtial, Nachupally (Kondagattu), Jagtial District (JNTUH CEJ) was established in the year 2007, as one of the Constituent colleges of JNTU Hyderabad with five different branches, viz. CSE, IT, ECE, EEE and ME, and has a team of experienced and dynamic Faculty and Staff.

I am glad to inform you that the ECE Department of JNTUH CEJ is organizing a National Level Technical Symposium (EMBLAZON'19) during April 2-3, 2019. Detailed information about the symposium is provided in the enclosed brochure. Several undergraduate, postgraduate students and faculty from various Engineering Colleges are expected to participate in this event.

I would appreciate if you would kindly give wide publicity and encourage your students and faculty to actively participate in the aforesaid symposium. I sincerely believe that participation in this event would also immensely help them to enhance their technical skills and conceptual knowledge.

I look forward to your support and cooperation in making this national symposium a success.

Thanking you and best regards,

Sincerely,

Prof. N.V. Ramana

**A
National Level
Technical Symposium**

EMBLAZON'19

April 2-3, 2019

Registration form

Please put tick on the event(s) to register

- Paper Poster Project
 Workshop Technical Quiz

- Title of Paper / Poster / Project
- Name
- College/ Institution
- Course of Study / Branch
- Address for Correspondence
- Email:
Mobile:
- Registration Fee Details
Amount DD No.
Date Name of Bank
- Accommodation required: Yes / No
- Signature of Applicant
- Recommendation of the Head of Dept./Institution
Date:
Place:

Chief Patron

Prof. A. Venugopal Reddy
Vice-Chancellor, JNTU Hyderabad

Patrons

Prof. A. Govardhan
Rector, JNTU Hyderabad

Prof. N. Yadaiah
Registrar, JNTU Hyderabad

Chairman

Prof. N.V. Ramana
Principal, JNTUHCEJ
Nachupally (Kondagattu), Jagtial Dist.

Vice-Chairman

Prof. S. Viswanadha Raju
Vice Principal, JNTUHCEJ

Convener

Dr. Dhiraj Sunehra
Professor & Head, Dept. of ECE

Co-Convener

Mr. S. Praveen Kumar, Asst. Prof., ECE

Student Coordinators

Mr. E. Aravind (Cell: 8309909737)
Mr. M. Yogesh (Cell: 6301783361)
Ms. B. Jhansi (Cell: 8074495689)
Ms. V. Sai Sreshtha (Cell: 7093851768)

Organising Committee

Sri. B. Prabhakar, Assoc. Prof., ECE
Smt. D. Naga Sudha, Asst. Prof., ECE
Smt. M. Tirupathamma, Asst. Prof., ECE
Dr. D. Ramesh, HoD, CSE
Dr. S. Suresh Kumar, HoD, IT
Sri. S. Jagadish Kumar, HoD, EEE
Dr. Suresh Arjula, HoD, ME
Dr. Y. Rajashekhar Reddy, HoD, Maths
Dr. Suresh Sripada, HoD, Physics
Dr. K. Vidya, HoD, Chemistry
Smt. M. Padmavathi, Deputy Registrar
Dr. T. Venugopal, CMO I/c.
Dr. B. Sathyanarayana, Hostel Manager
Dr. K. Vasantha Kumar, Transport I/c.
Dr. K. Srinivas, EMO I/c.
Sri. D.V.S. Satish, Executive Engineer I/c.

**A
National Level
Technical Symposium**

EMBLAZON'19

April 2-3, 2019



Organised by

**Department of
Electronics & Communication Engineering**

JNTUH College of Engineering Jagtial
Nachupally (Kondagattu)
Jagtial Dist. - 505 501, Telangana

College Website: www.jntuhcej.ac.in

About the College

JNTUH College of Engineering, Nachupally (Kondagattu), Jagtial Dist. (JNTUH CEJ) was established in the year 2007, as one of the Constituent colleges of JNTU Hyderabad with five different branches, viz. ECE, CSE, IT, EEE and ME. The College is committed to raise the intellectual tone of young students in understanding and incorporating the foundations of rapidly progressing changes in the fields of Science and Technology. The location of sprawling campus surrounded by lush green hills, temples and academic ambience upholds the traditional and cultural values among the students. JNTUH CEJ is located at Nachupally near Kondagattu which is about 22 km from Jagtial and about 38 km from Karimnagar.

About the Department

The Department of Electronics and Communication Engineering has a team of experienced and dynamic faculty and staff. The Department offers B.Tech course in Electronics & Communication Engineering and M.Tech course in Digital Systems and Computer Electronics.

About the Symposium

Keeping in view, the recent phenomenal advances in the field of Electronics and Communication Engineering and related application fields, it is proposed to conduct a National Level Technical Symposium EMBLAZON'19 during April 2-3, 2019. This Symposium aims at encouraging the participants to explore their technical skills and enhance their conceptual knowledge.

The technical program consists of various events including:

- ❖ Paper Presentation (Rostrum)
- ❖ Poster Presentation (Urdhva)
- ❖ Project Expo (Antharya)
- ❖ Technical Quiz (Technocrats)
- ❖ Two Day National Workshop on Electronic Circuit Simulation using NI Multisim Software
- ❖ Electronics Crossword
- ❖ Gyan Dhrushya (Video exhibits in ECE Domain)

Scope and Topics

The areas to be covered in the symposium include, but not limited to:

- ❖ Electronic Devices, Circuits and Systems
- ❖ Digital Electronics, VLSI and Embedded systems
- ❖ Microwave, Satellite and Optical Communications
- ❖ Signal Processing, Cellular & Mobile Communications
- ❖ Wireless Sensor Networks, Internet of Things
- ❖ Wearable Devices, Vehicular networks, Robotics
- ❖ Artificial Intelligence, Machine Learning, Data Science
- ❖ Power Systems, Renewable Energy Systems, Smart Grid
- ❖ Future Internet Applications & Services:
 - E-Health, Location Based Services, Social Networks

Abstract Submission

Authors are invited to send an electronic version of the abstract for paper / poster / project presentation typed in MS WORD in about 200 words on any topic related to the broad themes of the symposium, via email so as to reach us by 22nd March 2019. Acceptance will be communicated to the corresponding author by 25th March 2019.

Two Day National Workshop on Electronic Circuit Simulation using NI Multisim Software.

In association with M/s. Trident TechLabs Pvt. Ltd., Hyderabad.

- ❖ Introduction to NI Multisim Software
- ❖ Hands on Simulation: Transient Analysis of RLC Circuits, Transistor Amplifiers, Power Amplifiers,
- ❖ Oscillators, Applications of Operational Amplifiers

The Two-Day Workshop will be held during April 2-3, 2019 from 10:00 AM to 5:00 PM. The workshop will be practically oriented and number of registrations is limited to 60. Interested students and faculty are required to register in advance by sending the duly filled registration form along with Registration Fee of Rs.600/- addressed to the Convener.

Registration Fee

Paper/Poster/Project Expo
Rs.200/- (Two Hundred only) per head

Technical Quiz

Rs.50/- (Fifty only) per head

B.E./B.Tech, M.E./M.Tech Students in ECE, CSE, IT, EEE.

Two Day National Workshop on Electronic Circuit Simulation using Multisim Software

B.E./B.Tech, M.E./M.Tech Students in ECE, CSE, IT, EEE, and Faculty of Engineering College.

Rs.600/- (Six Hundred only) per head

Demand Draft should be drawn in favour of

"Convener, Emblazon, JNTUHCEJ" payable at Jagtial.

Important Dates

Deadline for Submission of Abstract	22nd March 2019
Intimation of Acceptance	25th March 2019
Deadline for Submission of Registration Form	29th March 2019

Venue

ECE DEPARTMENT (Academic Block-II)
JNTUHCEJ, Nachupally (Kondagattu), Jagtial

Time

10:00 AM to 5:00 PM

Accommodation

Accommodation for participants will be provided in College hostel/guest house on nominal payment.

Send the duly filled Registration form along with Registration fee to the following address:

Prof. Dhiraj Sunehra
Convener, Emblazon'19

JNTUH College of Engineering Jagtial
Nachupally (Kondagattu), Jagtial-505 501, Telangana
Ph: 7893278314, 9440268314
E-mail: emblazon2k19ece@gmail.com
Brochure can be downloaded from College Website
www.jntuhcej.ac.in

Emblazon'19 Inaugural function



JNTUH COLLEGE OF ENGINEERING JAGTIAL

Nachupally (Kondagattu), Jagtial Dist., Telangana

Department of
Electronics & Communication Engineering

Cordially invites you to the
Inaugural Function of

A National Level Technical Symposium

EMBLAZON'19

at 10:30 Hrs on Tuesday, the 2nd April, 2019
at Seminar Hall, Academic Block-II, JNTUH CEJ

Dr. N.V.Ramana
Professor of EEE & Principal, JNTUH CEJ
has graciously consented to be the Chief Guest

Dr. S. Viswanadha Raju
Professor of CSE & Vice Principal, JNTUH CEJ
Guest of Honour

Dr. Dhiraj Sunehra
Professor, Head of ECE Dept. & Convener
will preside over the function

Mr.E.Aravind, Mr.M.Yogesh
Ms. B.Jhansi, Ms.V.Sai Sreshtha
Student Coordinators

PROGRAMME

10:30 AM	Welcome (Student Coordinators)
10:45 AM	Lighting of Lamp followed by Prayer song by Sahithi and Sravanthi
10:55 AM	Convener's Report : Dr. Dhiraj Sunehra Professor, Head of ECE Dept. & Convener, Emblazon'19
11:05 AM	Address by the Vice Principal and Guest of honour Dr. S. Viswanadha Raju Professor of CSE & Vice Principal
11.15AM	Address by the Chief Guest: Dr. N.V. Ramana Professor of EEE & Principal
11:30 AM	Vote of Thanks Mr. S. Praveen Kumar Co-convener, Emblazon'19

A NATIONAL LEVEL STUDENT TECHNICAL SYMPOSIUM (EMBLAZON'19)

(April 2-3, 2019)

- Convener : Dr. Dhiraj Sunehra, Prof & Head of ECE
- Co-Convener : Mr. S. Praveen Kumar, Asst. Prof. of ECE

- Number of participants: 225 Students from all over the states of Telangana and Andhra Pradesh.

- **Events in EMBLAZON 2019:**

- **Technical Events:-**
 - **Two Day National Workshop on Electronic Circuit Simulation using NI Multisim Software**
 - **Paper Presentation**
 - **Poster Presentation**
 - **Project Expo**
 - **Technical Quiz**
 - **Electronics Crossword**
 - **Gyan Dhrushya**
 - **Exemplitia**

- **Non Technical Events:-**
 - **Student of the Year**
 - **Mathematical Tambola**
 - **The Sleuth Souls**
 - **Mock Placements**
 - **Decipher**
 - **Pubg**

Emblazon'19 – A National Level Technical Symposium

Inaugural Function



Paper Presentation by Students in Emblazon'19





Poster Presentation by Students in Emblazon'19



Sample Certificate – Emblazon’19



	JNTUH COLLEGE OF ENGINEERING JAGTIAL Nachupally (Kondagattu), Jagtial Dist.- 505 501, Telangana	
PARTICIPATION CERTIFICATE		
This is to certify that Mr./Ms. _____		
has participated in “ Paper Presentation ” conducted as part of EMBLAZON’19 - A National Level Technical Symposium organised by the Department of Electronics & Communication Engineering, JNTUH College of Engineering, Jagtial during April 2-3, 2019.		
Title of Presentation: _____		
Dr. DHIRAJ SUNEHRA Prof. & Head, ECE Convener, Emblazon’19		Dr. N.V. RAMANA Prof. of EEE & Principal Chairman, Emblazon’19

	JNTUH COLLEGE OF ENGINEERING JAGTIAL Nachupally (Kondagattu), Jagtial Dist.- 505 501, Telangana	
CERTIFICATE OF APPRECIATION		
This is to certify that Mr./Ms. _____		
has won the _____ prize in “ Paper Presentation ” conducted as part of EMBLAZON’19 - A National Level Technical Symposium organised by the Department of Electronics & Communication Engineering, JNTUH College of Engineering, Jagtial during April 2-3, 2019.		
Title of Presentation: _____		
Dr. DHIRAJ SUNEHRA Prof. & Head, ECE Convener, Emblazon’19		Dr. N.V. RAMANA Prof. of EEE & Principal Chairman, Emblazon’19

Two Day Workshop on Electronic Circuit Simulation using NI Multisim Software



Sample Certificate – Two Day Workshop on Electronic Circuit Simulation using NI
Multisim Software

	JNTUH COLLEGE OF ENGINEERING JAGTIAL	
Nachupally (Kondagattu), Jagtial Dist.- 505 501, Telangana		
PARTICIPATION CERTIFICATE		
This is to certify that Mr./Ms. _____		
has participated in the Two-Day National Workshop on “ Electronic Circuit Simulation using NI Multisim Software ” conducted during April 2-3, 2019 as part of EMBLAZON’19 - A National Level Technical Symposium organised by the Department of Electronics & Communication Engineering, JNTUH College of Engineering, Jagtial.		
Dr. DHIRAJ SUNEHRA Prof. & Head, ECE Convener, Emblazon’19	Dr. N.V. RAMANA Prof. of EEE & Principal Chairman, Emblazon’19	